

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Halahan, Patrick B.; Siniaguine, Oleg
Assignee: Tru-Si Technologies, Inc.
Title: Semiconductor Structures Having Multiple Conductive Layers In An
Opening, And Methods for Fabricating Same
Serial No.: Not Known Filing Date: Herewith
Examiner: Not Known Group Art Unit: Not Known
Docket No.: M-9999-1D US

11000 U.S. PTO
09/941447
08/28/01

San Jose, California
August 28, 2001

BOX PATENT APPLICATION
COMMISSIONER FOR PATENTS
Washington, D. C. 20231

INFORMATION DISCLOSURE STATEMENT
UNDER 37 CFR § 1.97(b)

Dear Sir:

Pursuant to 37 C.F.R. § 1.56, § 1.97 and § 1.98, the documents listed on the accompanying PTO Form-1449 are called to the attention of the Examiner for the above patent application. In addition, the Examiner's attention is called to U.S. Patent Application No. 09/456,225 filed December 6, 1999. Copies of these documents are enclosed.

Citation of these documents shall not be construed as:

1. an admission that the documents are necessarily prior art with respect to the instant invention;
2. a representation that a search has been made, or
3. an admission that the information cited herein is, or is considered to be, material to patentability as defined in § 1.56(b).

EXPRESS MAIL LABEL NO:

EL 884 815 018 US

Respectfully submitted,

Michael Shenker

Michael Shenker
Attorney for Applicant(s)
Reg. No. 34,250

LAW OFFICES OF
SKJERVEN MORRILL
MacPHERSON LLP

25 METRO DRIVE
SUITE 700
SAN JOSE, CA 95110
(408) 453-9200
FAX (408) 453-7979